

F. 6. 1

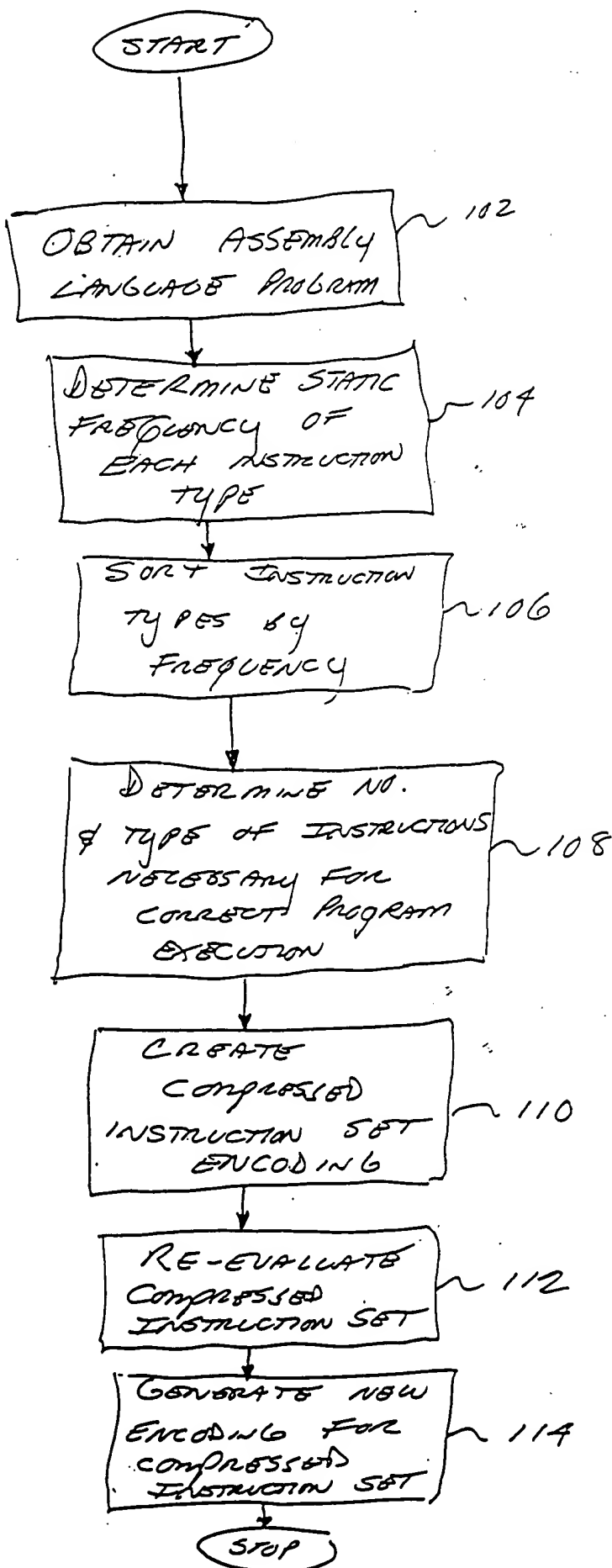




Fig. 2



Fig. 3

Fig. 2 and Fig. 3

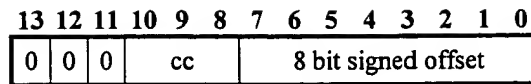


Fig. 4

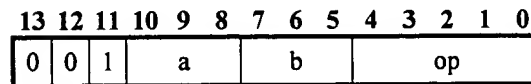


Fig. 5

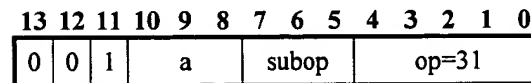


Fig. 6

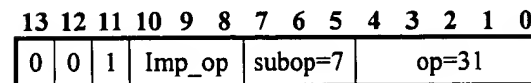


Fig. 7

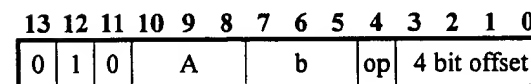


Fig. 8

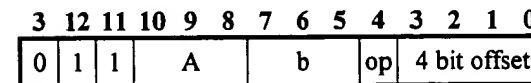


Fig. 9

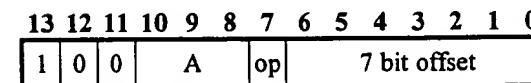


Fig. 10

Fig. 10, 9, 8, 7, 6, 5, 4, 3, 2, 1, 0

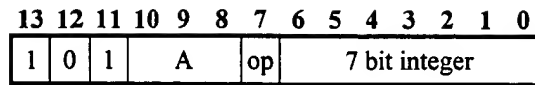


Fig. 11

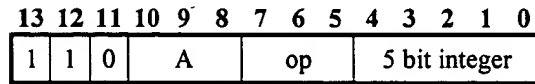


Fig. 12

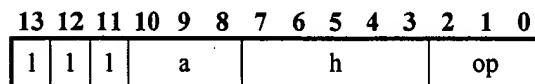


Fig. 13

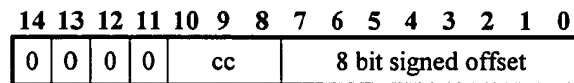


Fig. 14

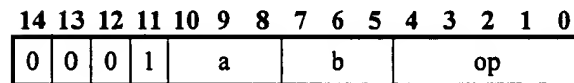


Fig. 15

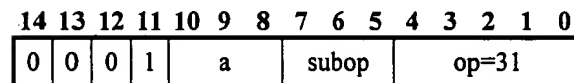


Fig. 16

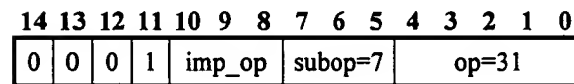


Fig. 17

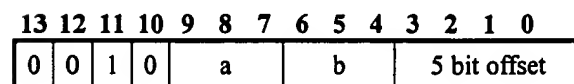


Fig. 18

Questions **A**nswers **I**nterests

Fig. 19

Fig. 20

Fig. 20

Fig. 21

Fig. 21

Fig. 22

Fig. 22

Fig. 23

Fig. 23

Fig. 24

Fig. 24

Fig. 25**Fig. 25**

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0	1	a		op		7 bit integer						

Fig. 26

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	1	0	a			b			op	4 bit offset			

Fig. 27

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	1	1	a			b			op	4 bit offset			

Fig. 28

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0	1	a			op	7 bit integer						

Fig. 29

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0	1	a			op	7 bit integer						

Fig. 30

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	1	1	0	11 bit signed offset										

Fig. 31

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	1	1	1	Reserved										

Fig. 32

F.6.33

FIG. 6.33

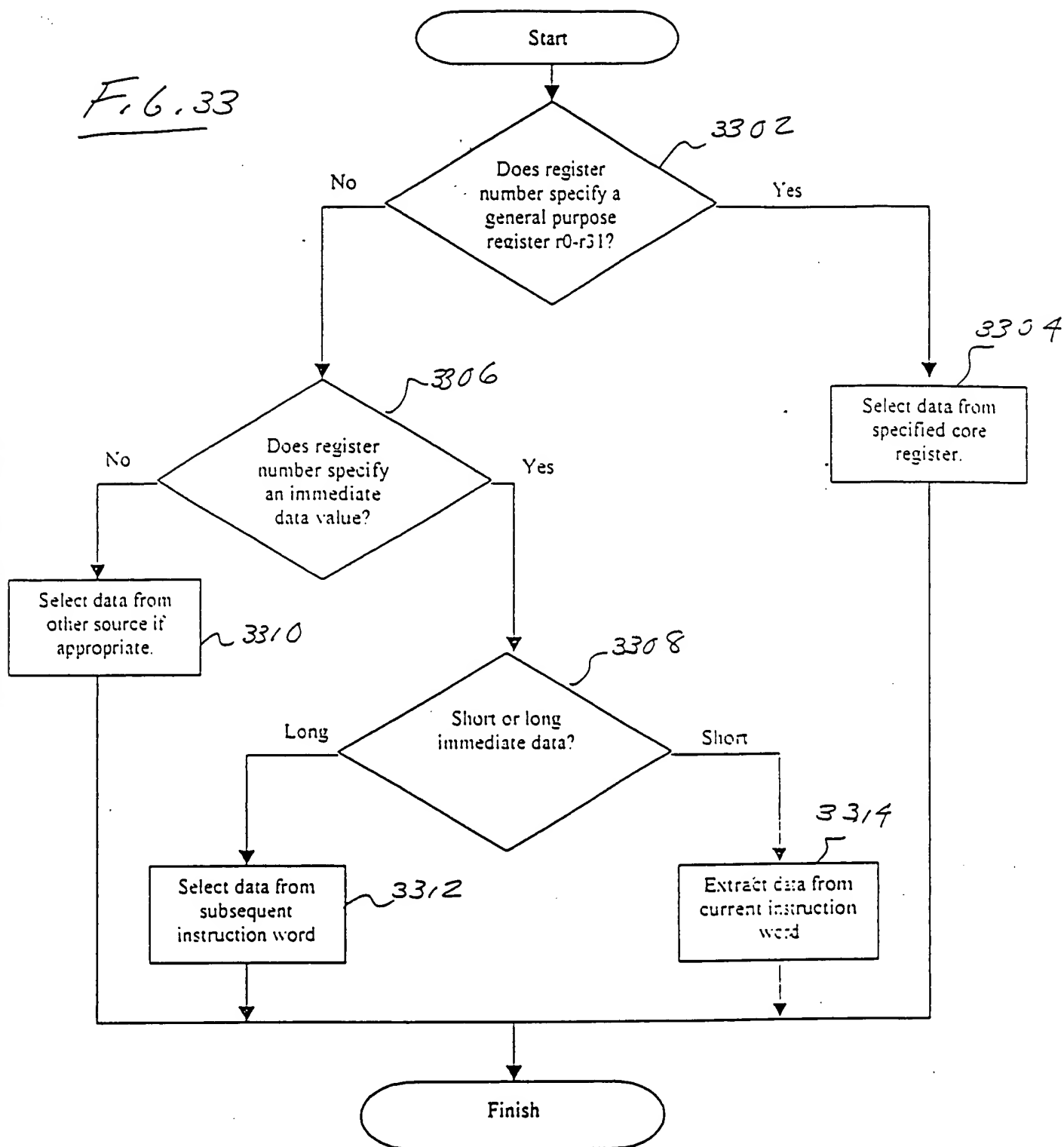


FIG. 35

